

Homework 1

Problem 1

We have introduced HCL in class to design combinatorial logic circuit. Try to write HCL code for the following circuit: Take A, B and C as input, output the median value of them.

E.g. Median (1, 3, 4) = 3.

The following three problems are based on the following code snippet:

1	0x000:	30f209000000	irmovl \$9, %edx	
2	0x006:	30f315000000	irmovl \$21, %ebx	
<u>3</u>	<u>0x00c:</u>	<u>30f480000000</u>	<u>irmovl \$128, %esp</u>	<u># Prob. 2</u>
4	0x012:	404364000000	rmmovl %esp, 100(%ebx)	# Store
<u>5</u>	<u>0x018:</u>	<u>a02f</u>	<u>pushl %edx</u>	<u># Prob. 3</u>
<u>6</u>	<u>0x01a:</u>	<u>b00f</u>	<u>popl %eax</u>	<u># Prob. 4</u>
7	0x01c:	7328000000	je done	# Not taken
8	0x021:	8029000000	call proc	
9	0x026:		done:	
10	0x026:	00	halt	
11	0x027:		proc:	
12	0x027:	90	ret	# Return

Problem 2

Fill in the right-hand column of the following table, specialize the function of each stage corresponding to the generic form.

Stage	Generic	Specific
	$\text{irmovl } V, rB$	$\text{irmovl } \$128, \%esp$
Fetch	$\text{icode:ifun} \leftarrow M_1[PC]$ $rA:rB \leftarrow M_1[PC + 1]$ $\text{valC} \leftarrow M_4[PC + 2]$ $\text{valP} \leftarrow PC + 6$	
Decode		
Execute	$\text{valE} \leftarrow 0 + \text{valC}$	
Memory		
Write Back	$R[rB] \leftarrow \text{valE}$	
Update PC	$PC \leftarrow \text{valP}$	

Problem 3

Fill in the right-hand column of the following table, specialize the function of each stage corresponding to the generic form.

Stage	Generic	Specific
	$\text{pushl } rA$	$\text{pushl } \%edx$
Fetch	$\text{icode:ifun} \leftarrow M_1[PC]$ $rA:rB \leftarrow M_1[PC + 1]$ $\text{valP} \leftarrow PC + 2$	
Decode	$\text{valA} \leftarrow R[rA]$ $\text{valB} \leftarrow R[\%esp]$	
Execute	$\text{valE} \leftarrow \text{valB} + (-4)$	
Memory	$M_4[\text{valE}] \leftarrow \text{valA}$	
Write Back	$R[\%esp] \leftarrow \text{valE}$	
Update PC	$PC \leftarrow \text{valP}$	

Problem 4

Fill in the right-hand column of the following table, specialize the function of each stage corresponding to the generic form.

Stage	Generic	Specific
	popl rA	popl %eax
Fetch	icode:ifun $\leftarrow M_1[PC]$ rA:rB $\leftarrow M_1[PC + 1]$ valP $\leftarrow PC + 2$	
Decode	valA $\leftarrow R[\%esp]$ valB $\leftarrow R[\%esp]$	
Execute	valE $\leftarrow valB + 4$	
Memory	valM $\leftarrow M_4[valA]$	
Write Back	R[%esp] $\leftarrow valE$ R[rA] $\leftarrow valM$	
Update PC	PC $\leftarrow valP$	

That's the end of Homework 1. You may find it easy to finish it. Anyway, enjoy the first weekend of this semester o(∩_∩)o