

ICS Homework 13

May 27, 2021

1 Organization

1.1 Virtual Memory 1

Which type of address is used in each of following scenarios, **virtual** or **physical** address?

The address of variables in C program	V
The address stored in a C pointer	V
The address of a C pointer	V
The address used in looking up L1 cache	P
The value in CR3	P
The address in L2 PTE	P
The address in L4 PTE	P
The value in PC register	V

1.2 Virtual Memory 2

The lookup of L1 cache usually consists of three steps:

1. locating the set
2. comparing the tag of each cache line in the set
3. returning bytes from cache or loading 1 value from next level memory system.

As the lookup uses physical address, the hardware can only start the cache lookup after address translation is completed. How to make cache lookup and address translation parallelized?

Show the requirements to the parameters of your paging and cache system.

ANS: Make sure the set bits of cache are in the page offset bits, so the hardware could locate the set and do address translation simultaneously. This requires $s + b \leq \text{len}(po)$

2 System Software

2.1

Which level would the following data being shared? Answer with **not shared**, **threads**, or **processes**.

For example, if X is shared between threads but not shared between processes, answer **threads**.

File descriptor table	threads
File table	processes
Stack	not shared
Heap	threads
Program counter	not shared
Condition code	not shared
Installed handler	threads
V-node table	processes