

ICS Homework 13

May 27, 2021

1 Organization

1.1 Virtual Memory 1

Which type of address is used in each of following scenarios, **virtual** or **physical** address?

The address of variables in C program	
The address stored in a C pointer	
The address of a C pointer	
The address used in looking up L1 cache	
The value in CR3	
The address in L2 PTE	
The address in L4 PTE	
The value in PC register	

1.2 Virtual Memory 2

The lookup of L1 cache usually consists of three steps:

1. locating the set
2. comparing the tag of each cache line in the set
3. returning bytes from cache or loading 1 value from next level memory system.

As the lookup uses physical address, the hardware can only start the cache lookup after address translation is completed. How to make cache lookup and address translation parallelized?

Show the requirements to the parameters of your paging and cache system.

2 System Software

2.1

Which level would the following data being shared? Answer with **not shared**, **threads**, or **processes**.

For example, if X is shared between threads but not shared between processes, answer **threads**.

File descriptor table	
File table	
Stack	
Heap	
Program counter	
Condition code	
Installed handler	
V-node table	